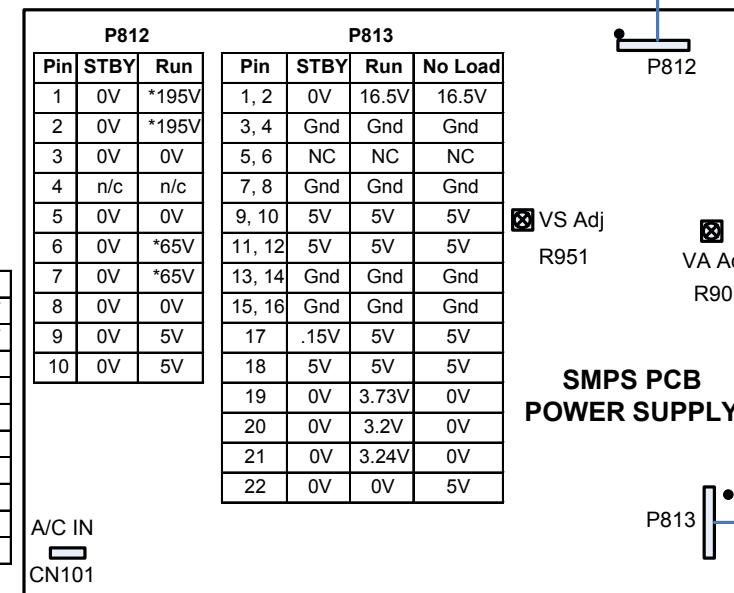
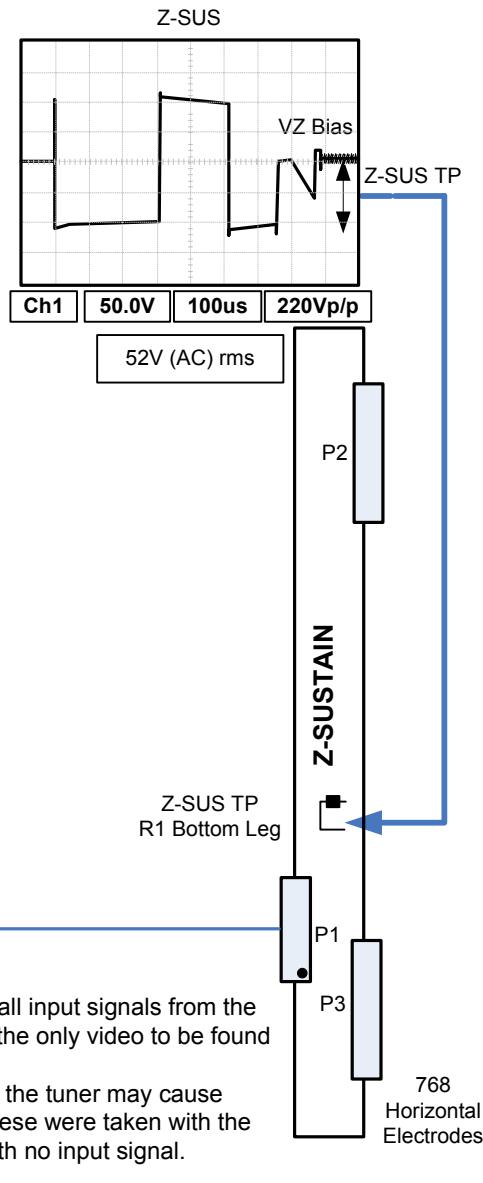


42PG20 CIRCUIT INTERCONNECT DIAGRAM



Remove all input signals from the unit so the menu will be the only video to be found on the LVDS cable.
NOTE: White noise from the tuner may cause these signals to vary. These were taken with the unit set to component with no input signal.

See next page for waveforms		
Pin	State	Ref#
11	Menu Off	10
12	Menu On	11
13	Menu Off	08
14	Menu On	09
15	Menu Off	12
16	Menu On	13
17	Menu Off	06
18	Menu On	07
19	Menu Off	14
20	Menu On	15
21	Menu Off	04
22	Menu On	05
23	Menu Off	16
24	Menu On	17
25	Menu Off	02
26	Menu On	03
27	Menu Off	18
28	Menu On	19
29	Menu Off	00
30	Menu On	01



IC702 5.29V 4) 5V 3) 0V 2) 0V 1) 0V
IC709 1) 3.29V 2) 1.26V 3) 0V
IC705 1) 3.3V 2) 1.8V 3) 0V
IC501 1) 3.3V 2) 3.29V 3) 0V
IC706 1) 5V 2) 2.6V 3) 0V
IC805 1) 5V 2) 3.3V 3) 0V
IC708 1) 5V 2) 3.3V 3) 0V
IC101 1) 5V 2) 3.3V 3) 0V
IC805 1) 5V 2) 3.3V 3) 0V

Volts per division

Time per division

Trigger offset

